

WEST Search History

DATE: Friday, September 16, 2005

Hide?	Set Name	Query	Hit Count
		<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>	
<input type="checkbox"/>	L20	20000301	40
<input type="checkbox"/>	L19	L18 and (chip or chipset or ic or integrated adj circuit)	113
<input type="checkbox"/>	L18	L17 and (attest\$4 or authenticat\$5)	189
<input type="checkbox"/>	L17	L16 and processor\$1 with (secure or isolated or dedicated) near3 (execution or process\$4)	257
<input type="checkbox"/>	L16	L15 l14 or l13	4794
<input type="checkbox"/>	L15	713/171,176,165,193,194.ccls.	3521
<input type="checkbox"/>	L14	726/2.ccls.	224
<input type="checkbox"/>	L13	726/4.ccls.	1104
<input type="checkbox"/>	L12	processor\$1 with (isolated or secure or dedicat\$4 or non\$1dedicat\$4) with (memory or storage) with (chip or chipset or integrated adj circuit or ic) with (attestation or authentication)	5
<input type="checkbox"/>	L11	processor\$1 with (isolated or dedicat\$4 or non\$1dedicat\$4) with (memory or storage) with (chip or chipset or integrated adj circuit or ic) with (attestation or authentication)	2
<input type="checkbox"/>	L10	processor\$1 with (dedicat\$4 or non\$1dedicat\$4) with (memory or storage) with (chip or chipset or integrated adj circuit or ic) with (attestation or authentication)	2
<input type="checkbox"/>	L9	processor\$1 with (dedicat\$4 or non\$1dedicat\$4) with (memory or storage) with (chip or chipset or integrated adj circuit or ic)	356
<input type="checkbox"/>	L8	L7 and (chip or chipset or ic or integrated adj circuit)	1254
<input type="checkbox"/>	L7	20000301	2380
<input type="checkbox"/>	L6	processor\$1 with (dedicat\$4 or non\$1dedicat\$4) with (memory or storage)	4719
<input type="checkbox"/>	L5	20000331	0
<input type="checkbox"/>	L4	L3 and chipset	14
<input type="checkbox"/>	L3	l1 and (attestation or authentication or validation or verification)	39
<input type="checkbox"/>	L2	l1 and remote with (attestation or authentication or validation or verification)	0
<input type="checkbox"/>	L1	processor\$1 with (isolated or non-isolated or normal) near4 execution near4 mode\$1	109

END OF SEARCH HISTORY